

# Senior Analog and Mixed-Signal IC Design Engineer

## Job Description:

As part of the design team, you will contribute to the development of world-class CMOS image sensors and systems-on-chip for high-end vision applications. After an internal training, you become responsible for the technical project lead of an image sensor. Your responsibilities include the architectural definition and top-level specification of the chip and the design implementation and verification in accordance with CMOSIS' design flow. This further includes specification of the building blocks of the design, design documentation and supervision of the analog-and mixed signal chip verification.

You will lead a small team for the design of the chip and work in close collaboration with the project managers for an on-time design delivery and chip validation. You also develop yourself some of the key building blocks of the image sensor and monitor the designs of your project team members. You present your work to our international customers and interact with them on technical specifications and design implementation. You interact with your peers and the technical staff in design reviews, and during the development of critical and new circuits and building blocks.

Over time, we expect you to contribute to our innovations in the field of image sensors and improve the related skills of the design and layout engineers in your project team.

## Minimum Requirements:

- Master degree in Micro-electronics Engineering or equivalent by experience.
- At least 4-5 years of hands-on experience in custom analog and mixed signal CMOS IC design.
- Good understanding of analog and custom mixed-mode layout techniques (IR drops & parasitic coupling, matching, shielding, supply domain isolation,... ).
- Expert knowledge of certain building blocks, like opamps, ADC, PLL, bandgap, high speed output drivers, ...
- Experience in mixed-signal simulations.
- Fluent written and verbal communication skills in English. Dutch or Portuguese is a plus.
- You are a reliable team player and efficient communicator, able to coach and mentor a technical project team

## Additional valued competences:

- Experience with image sensor design
- Experience in VHDL RTL design and verification of module and chip-level designs
- Affinity with device physics or pixel design or design of electro-optical sensors
- Affinity with the whole IC and product development cycle, including specification, DFT, DFMEA, back-end integration, manufacturing, and system integration

## Our offer:

- A permanent position

- An attractive and challenging environment where you can contribute significantly to advance the state-of-art in CMOS image sensors and the growth of the company
- A pleasant work environment in an informal atmosphere
- You work with state-of-the-art IC design tools from Cadence, Mentor Graphics and Synopsys
- Extensive hand-on guidance from world-class image sensor experts and the opportunity to become one.

**Location:** Antwerp, Belgium or Funchal, Portugal with initial 6 to 9 month training in the Antwerp Design Center.

**Contact:** [HR@AWAIBA.COM](mailto:HR@AWAIBA.COM)



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