

Analog and Mixed-Signal IC Design Engineer

Job Description:

As part of the design team, you contribute to the development of world-class CMOS image sensors and systems-on-chip for high-end vision and imaging applications. You are responsible for the development of key analog building blocks of our image sensors and contribute to the top-level design. Your responsibilities include circuit design, implementation, layout, documentation and analog and mixed-signal verification. You participate in design reviews and in development reviews with our international customers.

You work in close collaboration with the peer engineers, technical team leaders and project managers. You gather the knowledge to become an image sensor design expert by on-the-job training, in-house and external training and participation to conferences and courses.

Minimum Requirements:

- Master degree in Micro-electronics Engineering or equivalent by experience
- Good understanding of analog CMOS design
- Good understanding of analog and custom mixed-mode layout techniques (IR drops & parasitic coupling, matching, shielding, supply domain isolation, ...).
- Experience in the design of analog building blocks, like opamps, ADC, PLL, bandgap, high speed output drivers, ...
- You are hands-on, accurate, self-critical and eager to learn
- Fluent written and verbal communication skills in English. Dutch or Portuguese is a plus.
- You are a reliable team player and efficient communicator

Additional valued competences:

- At least 2 years of hands-on experience in custom analog and mixed signal CMOS IC design
- Experience in mixed-signal simulations and verification
- Experience with image sensor design
- Affinity with the whole IC and product development cycle, including specification, DFT, DFMEA, back-end integration, manufacturing, and system integration

Our offer:

- A permanent position
- An attractive and challenging environment where you can contribute significantly to advance the state-of-art in CMOS image sensors and the growth of the company
- A pleasant work environment in an informal atmosphere
- You work with state-of-the-art IC design tools from Cadence, Mentor Graphics and Synopsys
- Extensive hand-on guidance from world-class image sensor experts and the opportunity to become one.

Location: Antwerp- Belgium or Funchal- Portugal with initial 6-month training in the Antwerp Design Center.

Contact: HR@awaiba.com