

Senior Digital IC design engineer

Job Description:

As part of the design team, you contribute to the development of world-class CMOS image sensors and systems-on-chip for high-end vision and imaging applications. As a digital designer, you are responsible for the development, implementation, and verification of on-chip control and data processing logic and the design of functional digital verification models. You present your work to our international customers and interact with them on technical specifications. You work in close collaboration with the peer engineers, technical team leaders and project managers. You gather the knowledge to become an image sensor design expert by on-the-job training, in-house and external training and participation to conferences and courses. Depending on your experience and personal interests you can grow into architecture development, physical and back-end design, design methodology development, and/or technical project management.

Minimum Requirements:

- Master degree in Electronics Engineering
- 5 years of hands-on experience in an ASIC or FPGA design position
- Experience in VHDL RTL design and verification of module and chip-level designs
- Experience in synthesizing RTL code for ASIC using common EDA tools
- Affinity with digital Place and Route and timing closure
- Accurate, self-critical, hands-on and self-managed person
- Fluent written and verbal communication skills in English. Dutch or Portuguese is a plus.
- You are a reliable team player and efficient communicator

Additional valued competences:

- Experience with image sensor design
- Basic understanding of Verilog language
- Basic understanding of analog circuit design
- Experience with Cadence schematic editor
- Experience with mixed-mode simulation (analog-digital)
- Affinity with revision control systems
- Affinity with the whole IC and product development cycle, including specification, DFT, DFMEA, back-end integration, manufacturing, and system integration

Our offer:

- A permanent position
- An attractive and challenging environment where you can contribute significantly to advance the state-of-art in CMOS image sensors and the growth of the company
- A pleasant work environment in an informal atmosphere
- You work with state-of-the-art IC design tools from Cadence and Mentor Graphics.
- Extensive hand-on guidance from world-class image sensor experts and the opportunity to become one.

Location: Antwerp, Belgium or Funchal, Portugal with initial 6-month training in the Antwerp Design Center.

Contact: HR@AWAIBA.COM



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